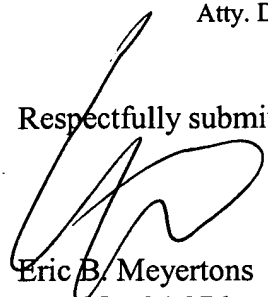


Inventors: Bois et al.
Application No.: 10/089,588
Atty. Dkt. No.: 5972-00300

Respectfully submitted,



Eric B. Meyertons
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Date: July 1, 2005



PATENT

2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/089,588

Confirmation No.: 2551

Filing Date: July 15, 2002

Inventors: Bois et al.

Title: SEMICONDUCTOR DEVICE
COMBINING THE
ADVANTAGES OF
MASSIVE AND SOI
ARCHITECTURE, AND
METHOD FOR MAKING
SAME

Examiner: L. Pham

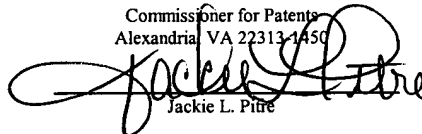
Art Unit: 2814

Atty. Dkt. No.: 5972-00300/EBM

CERTIFICATE OF MAILING
UNDER 37 C.F.R. §1.8

DATE OF DEPOSIT: July 1, 2005

I hereby certify that this correspondence is being deposited with
the United States Postal Service with sufficient postage as first
class mail on the date indicated above and is addressed to:

Commissioner for Patents
Alexandria, VA 22313-1450
Jackie L. Pifre**AMENDMENT; RESPONSE TO OFFICE ACTION MAILED JUNE 7, 2005**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this
paper.

Remarks begin on page 7 of this paper.

07/13/2005 EFLORES 00000070 501505 10089588

01 FC:1202 50.00 DA